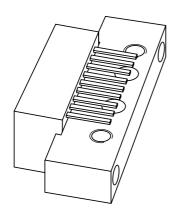
DISCRETE SEMICONDUCTORS

DATA SHEET



BGE885 860 MHz, 17 dB gain push-pull amplifier

Product specification Supersedes data of 1999 Mar 30

2001 Oct 31





Philips Semiconductors

860 MHz, 17 dB gain push-pull amplifier

BGE885

FEATURES

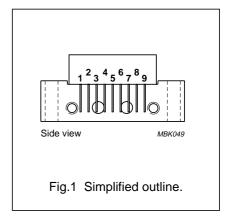
- · Excellent linearity
- Extremely low noise
- Rugged construction
- TiPtAu metallized crystals ensure optimal reliability.

DESCRIPTION

Hybrid amplifier module for use in CATV systems operating over a frequency range of 40 to 860 MHz with a voltage supply of 24 V (DC).

PINNING - SOT115D

PIN	DESCRIPTION		
1	input; note 1		
2	common		
3	common		
4	12 V, 60 mA supply terminal		
5	common		
6	common		
7	common		
8	+V _B		
9	output; note 1		



Note

1. Pins 1 and 9 carry DC voltages.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Gp	power gain	f = 50 MHz	16.5	17.5	dB
I _{tot}	total current consumption (DC)	V _B = 24 V	_	240	mA

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
V _B	DC supply voltage	_	28	V
Vi	RF input voltage	_	65	dBmV
T _{stg}	storage temperature	-40	+100	°C
T _{mb}	operating mounting base temperature	-20	+100	°C

860 MHz, 17 dB gain push-pull amplifier

BGE885

CHARACTERISTICS

Bandwidth 40 to 860 MHz; V_B = 24 V; T_{mb} = 30 °C; Z_S = Z_L = 75 Ω

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Gp	power gain	f = 50 MHz	16.5	17.5	dB
SL	slope cable equivalent	f = 40 to 860 MHz	0.2	1.2	dB
FL	flatness of frequency response	f = 40 to 860 MHz	_	±0.5	dB
S ₁₁	input return losses	f = 40 to 450 MHz	14	_	dB
		f = 450 to 860 MHz	10	_	dB
S ₂₂	output return losses	f = 40 to 450 MHz	14	_	dB
		f = 450 to 860 MHz	10	_	dB
d ₂	second order distortion	note 1	_	-53	dB
Vo	output voltage	d _{im} = −60 dB; note 2	59	_	dBmV
F	noise figure	f = 350 MHz	_	7.5	dB
		f = 860 MHz	_	8	dB
I _{tot}	total current consumption (DC)	note 3	_	240	mA

Notes

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1. f_p = 349.25 \text{ MHz}; V_p = 59 \text{ dBmV};

f_q = 403.25 \text{ MHz}; V_q = 59 \text{ dBmV};

measured at f_p + f_q = 752.5 \text{ MHz}.
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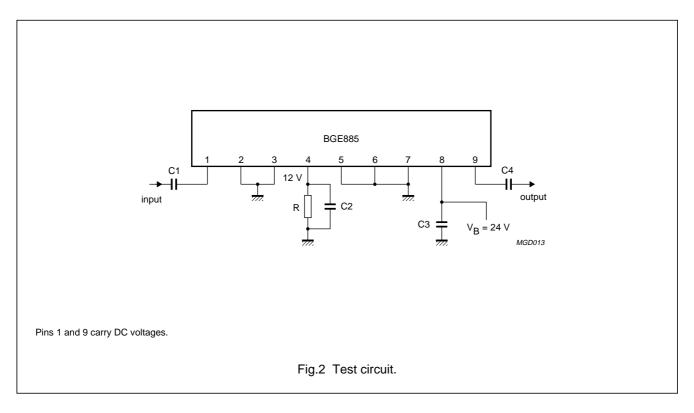
2. Measured according to DIN45004B:

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Measured according to DIN45004B: f_p = 851.25 \text{ MHz}; V_p = V_o = 59 \text{ dBmV}; f_q = 858.25 \text{ MHz}; V_q = V_o - 6 \text{ dB}; f_r = 860.25 \text{ MHz}; V_r = V_o - 6 \text{ dB}; measured at f_p + f_q - f_r = 849.25 \text{ MHz}.
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3. The module normally operates at V_B = 24 V, but is able to withstand supply transients up to 30 V.

860 MHz, 17 dB gain push-pull amplifier

BGE885



List of components (see Fig.2)

COMPONENT	DESCRIPTION	VALUE
C1, C3, C4	ceramic multilayer capacitor	1 nF
C2	ceramic multilayer capacitor	1 nF (max.)
R	resistor	200 Ω,1 W

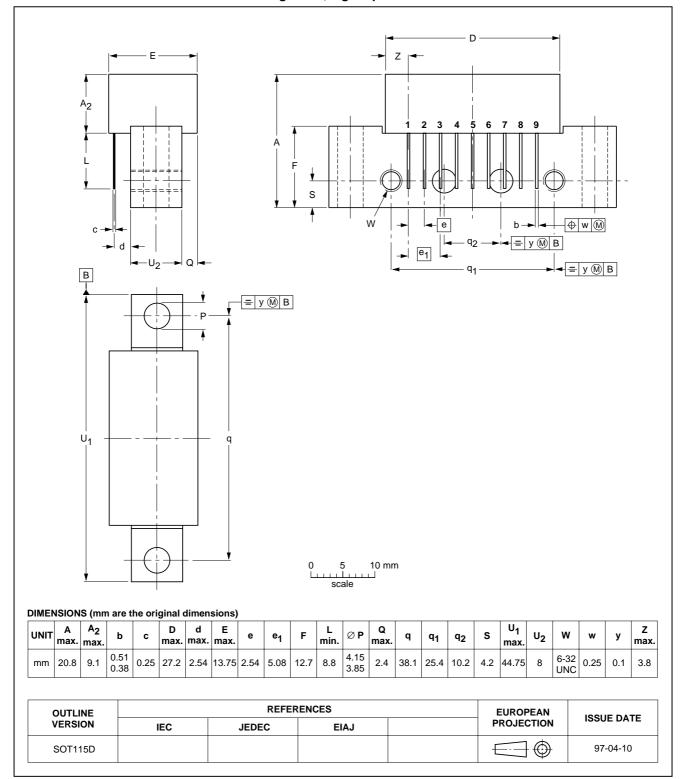
860 MHz, 17 dB gain push-pull amplifier

BGE885

PACKAGE OUTLINE

Rectangular single-ended package; aluminium flange; 2 vertical mounting holes; 2 x 6-32 UNC and 2 extra horizontal mounting holes; 9 gold-plated in-line leads

SOT115D



860 MHz, 17 dB gain push-pull amplifier

BGE885

DATA SHEET STATUS

DATA SHEET STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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860 MHz, 17 dB gain push-pull amplifier

BGE885

NOTES

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Contact information

For additional information please visit http://www.semiconductors.philips.com. Fax: +31 40 27 24825 For sales offices addresses send e-mail to: sales.addresses@www.semiconductors.philips.com.

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